



PCT/TO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
ARC.001AAPPLICATION NO.
09/418,663INFORMATION DISCLOSURE STATEMENT
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(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT
Hakewill, et al.FILING DATE
October 14, 1999GROUP
2123

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
EG	1	5,361,373 ✓	11/1/94	Gilson			
4	2	5,450,586 ✓	9/12/95	Kuzara et al.			
	3	5,535,331 ✓	7/9/96	Swoboda et al.			
	4	5,696,956 ✓	12/9/97	Razdan et al.			
	5	5,748,875 ✓	5/5/98	Tzori			
	6	5,819,050 ✓	10/6/98	Boehling et al.			
	7	5,819,064 ✓	10/6/98	Razdan et al.			
	8	5,854,929 ✓	12/29/98	Van Praet et al.			
	9	5,870,588 ✓	2/9/99	Rompaey et al.			
	10	5,999,734 ✓	12/7/99	Willis et al.			
	11	6,006,022 ✓	12/21/99	Rhim et al.			
	12	6,035,123 ✓	3/7/00	Razdan et al.			
	13	6,182,206 B1 ✓	1/30/01	Baxter			
	14	6,195,593 B1 ✓	2/27/01	Nguyen			
	15	6,226,776 B1 ✓	5/1/01	Panchul et al.			
	16	6,385,757 B1 ✓	5/7/02	Gupta et al.			
	17	6,408,428 B1 ✓	6/18/02	Schlansker et al.			
	18	6,457,173 B1 ✓	9/24/02	Gupta et al.			
8	19	6,477,683 B1 ✓	11/5/02	Killian, et al.			
EG	20	6,477,697 B1 ✓	11/5/02	Killian, et al.			

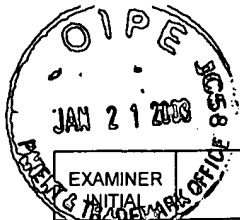
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
EG	21	2 308 470 ✓	6/97	GB				



EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
E C	22	Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039
	23	"Custom-Fit Processors: Letting Applications Define Architectures" by Josh Fisher, Paolo Faraboschi, and Giuseppe Desoli
	24	MDR - Microprocessor Report Article entitled "Tensilica Patents Raise Eyebrows" by Tom R. Halfhill dated 12/9/02
	25	Hartoog et al, "Generation of Software Tools from Processor Descriptions for Hardware/Software Codesign," ACM, Jun. 1997, pp. 303-306.*
	26	Freericks "The nML Machine Description Formalism" (Bericht 1991/15 pp. 3-41).
	27	Fauth et al. "Describing Instruction Set Processors Using nML" (Proc. Euro. Design & Test Conf., Paris, Mar. 1995, IEEE 1995, 5 pp.).
	28	Internet Publication http://www.retarget.com/brfchschk.html (19 pp) No date.
	29	Internet Publication http://www.synopsys.com/products/designware/8051_ds.html (8 pp) No date.
	30	Internet Publication http://www.synopsys.com/products/designware/dwpci_ds.html (16 pp) No date.
	31	Internet Publication http://www.lexra.com/product.html (11 pp) No date.
E C	32	Internet Publication http://www.risccores.com/html/body_aboutarc.htm (13 pp) No date.
	33	Clucas, R., "Designing with a customisable microprocessor core," Electronic Eng'g, vol. 71, No. 865, Feb. 1, 1999, p. 35.
	34	Hogl, H., et al., "Enable + +: A General-Purpose L2 Trigger Processor," Nuclear Science Symp. & Medical Imaging Conf., vol. 2, Oct. 21-28, 1995, pp. 667-671.
	35	Wollan, V., "A Design Methodology Achieving Fast Development Cycles for Complex VLSI Architectures," Proc. on the European Conf. on Design Automation, Feb. 22-Mar. 25, 1993, pp. 532-535.
	36	Tensilica, Inc; Xtensa Instruction Set Architecture (ISA) Reference Manual Revision 1.0. (First 25 pages)
	37	Tensilica, Inc. Instruction Extension Language (TIE) Reference Manual Revision 1.3.
	38	Compton et al., "Configurable Computing: A Survey of Systems and Software," Technical Report, Northwestern University, Dept. of ECE, 1999.
	39	Hauck et al., "The Chimaera Reconfigurable Functional Unit." Proceedings of the IEEE Symposium on Field-Programmable Custom Computing Machines, 1997.
	40	Razdan et al., "A High-Performance Microarchitecture with Hardware-Programmable Function Units," Proceedings of MICRO-27, Nov. 1997.
	41	Wang et al., "Hardware/Software Instruction Set Configurability for System-on-Chip Processors," Proceedings of Design Automation Conference, 2001.

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EXAMINER	ED GARCIA-GTEPR	DATE CONSIDERED	2/2/03
<p>*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.</p>			